

Readme for 82578 GbE NVM

Care should be taken to use the correct GbE NVM (Non-Volatile Memory) firmware revision for the stepping combination of the Ibex Peak and the 82578. The table below lists the NVM revision that is optimized for use with the silicon stepping combination.

NOTE: Using a newer revision NVM with an older silicon stepping or older revision NVM with a newer silicon stepping could cause system instability and unpredictable behavior.

IBX Stepping	82578 Stepping/PHY-Ver ¹	Corporate (82578DM) or Consumer (82578DC) Sku	NVM Version	Comments
B2/B3	C0 = 0x42	DM	0.C2	Please do not forget to upgrade to Release 14.6 Driver version 11.2.19.0
		DC	0.C5	

1. See below for detail on PHY versions.

How to identify the PHY stepping in the system?

Use the latest version of LANCONF (Intel LAN tool found in the SVK (Sample Validation Kit)). Navigate to *Registers MAC and PHY Registers -> PHY Registers Menu -> Raw PHY Registers*. On the *Raw PHY Registers* screen, reference the value of the lower byte of register 0x0003. A value of 0x41 indicates B-0 stepping and 0x42 indicates C-0 stepping.

```

C:\Users\user\Desktop\lanconf\Win32\lanconfW32.exe
[ Raw PHY Registers ]

[PHY Registers]          [PHY Details]

0x0000 3140 0x0010 0862 PHY Address: 0x00000002
0x0001 7949 0x0011 0010 PHY OUI: 0x001374
0x0002 0040 0x0012 0000 PHY Model: 0x04
0x0003 0042 0x0013 0000 PHY Revision: 0x2
0x0004 01E1 0x0014 002C PHY Vendor: Intel-Ath
0x0005 0000 0x0015 0000 PHY Page: 0x0
0x0006 0004 0x0016 0000
0x0007 2001 0x0017 0000
0x0008 0000 0x0018 3200
0x0009 0200 0x0019 0000
0x000A 0000 0x001A 0000
0x000B 0000 0x001B 0000
0x000C 0000 0x001C 0000
0x000D 0000 0x001D 0000
0x000E 0000 0x001E 02EE
0x000F 2000 0x001F 0000

[F7] Set PHY Page [F8] Set Phy Address
[F3] Dump to File [F5] Refresh Screen [ESC] Exit

LANConf v1.13.29.0 DI:10EF <B/D/F:0/25/0> [F1]-Help [

```

How to identify the Ibex Peak stepping?

Use a PCI tool to read the LPC Device ID D31:f0 offset 08h. If the byte value in the field reads:

02h: IBX A1 stepping

03h: IBX B0 stepping

04h: IBX B1 stepping

05h: IBX B2 stepping